

OCT 21 2005

Attorney's Docket No.: MP0346 / 13361-068001

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Number of pages including this page 2

Applicant : Tsai, et al.
Serial No. : 10/830,117
Filed : April 21, 2004

Art Unit : 2816
Examiner : Unknown


Title : ITERATIVE FILTER CIRCUIT CALIBRATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is a Status Inquiry, faxed this 21st day of October, 2005, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: October 21, 2005


Mark D. Kirkland
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Commissioner for Patents
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STATUS INQUIRY

Applicant respectfully inquires as to the status of the above application. The application was filed on April 21, 2004.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: October 21, 2005


Mark D. Kirkland
Reg. No. 40,048

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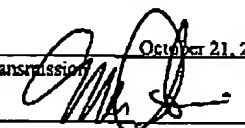
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